

### **Evaluation Process and Reforms**

2.5.1 Mechanism of internal/ external assessment is transparent and the grievance redressal system is time- bound and efficient

### **Response:**

CBIT is affiliated to Visvesvaraya Technological University, Belagavi, Karnataka. The University regulates the guidelines for the internal and external assessment. Through induction activities and the first-year students are made aware of the evaluation process. The university permits 20% for internal marks and 80% for end-of-semester exams depending on the schemes as per the university's evaluation timetable, the internal assessment results are periodically uploaded to the university website.

### At Institution Level:

Every semester, three internal assessment tests (IAT) are given. The Academic Calendar for every semester includes information about the dates of the Internal assessment tests. One week before the Internal Assessment Test starts, the schedule for the same is made public. Even for internal assessment exams, the sitting arrangement and table markings are adhered as per main exams. Faculty members are instructed to set their own course questions, and four days before each exam, all question papers that have been properly signed by the Department head and Principal are returned the internal test coordinator for conduction of test. Parents receive progress report with the internal marks of their wards when the internal assessment test is successfully completed. Any discrepancies in the student's internal assessment test are rectified by the internal test Coordinator and Head of the department.

#### **Challenge Evaluation:**

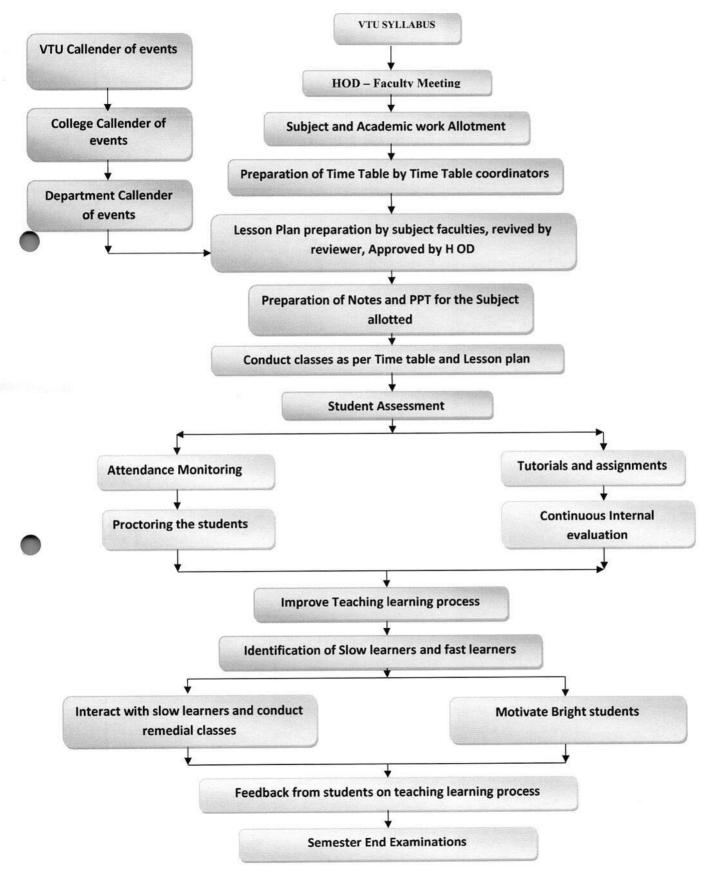
If the re-evaluation results are not satisfactory, a student can apply for challenge evaluation within a week after the announcement of the results. This evaluation process is carried out by an expert appointed by the University.

(Approved by AICTE, New Delhi, Recognized by Govt. of Karnataka & Affiliated to VTU, Belagavi.)

KOLAR – SRINIVASAPUR ROAD, KOLAR – 563101, KARNATAKA.

E-Mail: cbitkolar@gmail.com, Website: www.cbitkolar.edu.in Mobile No: 6360281836

### Mechanism of Internal External Examination



C BYREGOWDA INSTITUTE OF TECHNOLOGY (Approved by AICTE, New Delhi, Recognized by Govt. of Karnataka & Affiliated to VTU, Belagavi.) >KOLAR – SRINIVASAPUR ROAD, KOLAR – 563101, KARNATAKA.

E-Mail: cbitkolar@gmail.com, Website: www.cbitkolar.edu.in Mobile No: 6360281836

METRIC	PARAMETER	LINK TO THE RELEVANT DOCUMENTS
2.5.1	Mechanism of internal/ external assessment is transparent and the grievance redressal system is time- bound and efficient Vie	View



C BYREGOWDA INSTITUTE OF TECHNOLOGY (Approved by AICTE, New Delhi, Recognized by Govt. of Karnataka & Affiliated to VTU, Belagavi.) KOLAR – SRINIVASAPUR ROAD, KOLAR – 563101, KARNATAKA. E-Mail: cbitkolar@gmail.com, Website: www.cbitkolar.edu.in Mobile No: 6360281836

## 2. 5.1 MECHANISM OF INTERNAL/ EXTERNAL ASSESSMENT IS TRANSPARENT AND THE GRIEVANCE REDRESSAL SYSTEM IS TIME BOUND AND EFFICIENT.

SL. No	Particulars	Page no
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## KOLAR

## CALENDAR OF EVENTS FOR 7<sup>TH</sup> SEMESTER BE 2023-24.

EVENTS	7 <sup>TH</sup> SEM BE (18 SCHEME)
COMMENCEMENT OF SEMESTER	14/08/2023
INTERNSHIP	14/08/2023 to 09/09/2023
COMMENCEMENT OF CLASSES	11/09/2023
1 <sup>st</sup> IA TEST	2 <sup>nd</sup> ,3 <sup>rd</sup> and 6 <sup>th</sup> NOV 2023
PROCTOR REPORT 1	9 <sup>th</sup> NOV 2023
2 <sup>nd</sup> IA TEST	4th ,5th and 6th DEC 2023
PROCTOR REPORT 2	10 <sup>th</sup> DEC 2023
3 <sup>rd</sup> IA TEST	3 <sup>rd</sup> ,4 <sup>th</sup> and 5 <sup>th</sup> JAN 2024
LAST WORKING DAY	06/01/2024
PRACTICAL EXAMINATION	08/01/2024 TO 19/01/2024
THEORY EXAMINATION	22/01/2024 TO 09/02/2024
COMMENCEMENT OF NEXT SEMESTER	13/02/2024

List of General Holidays

Date	Festival
18/09/2023	GANESH CHATURTHI
28/09/2023	ED MEALAD
02/10/2023	GANDHI JAYANTHI
14/10/2023	MAHALAYA AMVASYA
23/10/2023	AYUDHA POOJA
24/10/2023	VIJAYA DHASHAMI
01/11/2023	RAJYOTSAVA DAY
13/11/2023	DEEPAVALI
14/11/2023	BALIPADYAMI
30/11/2023	KANAKADASA JAYANTHI
25/12/2023	CHRISTMAS

**NOTE:** FIRST, THIRD Saturday of every month will be holiday

SD/-Dr. APRAMEYAN S Principal

## C BYREGOWDA INSTITUTE OF TECHNOLOGY DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DATE: 02/02/2024

## CIRCULAR

It is hereby informed to all the 5<sup>th</sup> Semester students that 2<sup>nd</sup> Internal Assessment is scheduled from February 8<sup>th</sup> to February 10<sup>th</sup> 2024. And the following guidelines to be maintained for the upcoming internals.

1. The students should have 75% of attendance compulsory.

2. The students should have cleared the fees as mentioned below.

- CET Students should clear the complete fees of the Academic year.
- Management Students should clear 50% of their college fees (Current Academic year).
- > SNQ students should clear the complete fees of the Academic year.

Co-ordinators

CBIT-KOLAR

Prepared Date: 02/02/2024 06#Form#01- Rev No.00

TEST TIME TABLE NO: 2 <sup>nd</sup> INTERNALS [5 <sup>th</sup> SEM]	DEPARTMENT: COMP	UTER SCIENCE AND ENGINEERING / AIML
	TEST TIME TABLE NO	and DITERVELAND ENGINEERING / AIML

Date>>>	08/02/2024		09/02/20	24	10/02/2024				
Semester>>	V:								
Session>>>	MS	AS	MS	AS	MS	AS			
Time>>>>	10.00AM - 11.30AM	02.00PM - 03.30PM	10.00AM - 11.30AM	02.00PM - 03.30PM	10.00AM 11.30AM	2.00PM - 03.30PM			
Subject Name	ATC	CN	DBMS	AIML	RM	EVS			
Subject Code	21CS51	21CS52	21CS53	21C854 / 21A154	21CS56	21CIV57			

PREPARED BY (Test Coordinator)

.

REVI EWED BY (ISO Coordinator)

APPROVED BY HOD

APPROVED BY PRINCIPAL

CBIT-Kolar		Form for INTERNAL ASSESM	IENT PROCESS
Written By	Management representative	Doc. No 06#Form#02b	Rev No. 00
Approved By	Principal	Effective date: 02-11	Page 1 of 2

## Internal Test Question Paper Format-CBCS scheme

Name of the Staff/s: Sarika C G Date: 02.11.2023

Signature:

Reviewer's Signature

NOTE: Only the following information to be given to the students

### C BYREGOWDA INSTITUTE OF TECHNOLOGY Department: Computer Science and Engineering B.E -Test: I

Semester: VII ubject name and code: Artificial Intelligence and Machine Learning Duration: 90 minutes

Section: A Subject code: 18CS71 Max Marks: 50

Answer the following question selecting ONE full question from each PART

Q	No		Marks	COs	Levels
		Part A	•		
	a)	Define artificial intelligence? List and explain various task domains of AI	02+08	C01	L1
1	b)	A water jug problem states "you are provided with two jugs, first one with 4-gallon capacity and the second one with 3-gallon capacity. Neither have any measuring markers on it." How can you get exactly 2 gallons of water into 4-gallon jug? a. Write down the production rules for the above problem. b. Write any one solution to the above problem.	10	CO2	L2
	c)	Explain A* algorithm	10	CO2	L2
		OR	L	1	
	a)	Briefly discuss AI problem characteristics.	10	C01	L1
	b)	Explain AO* algorithm.		CO2	L2
2	c)	i) Write an algorithm for Steepest-Ascent hill climbing.	03	CO2	L2
		ii) Crypt arithmetic problem: SEND + MORE = MONEY. Initial state: No two letters have same value. Sum of digits must be shown	07	CO2	L1
		Part B		<b>I</b>	
3	a)	<ul> <li>Consider the following sentences</li> <li>Marcus was a man</li> <li>Marcus was a Pompeian</li> <li>All Pompeian were Romans</li> <li>Caesar was a ruler</li> <li>Caesar was a ruler</li> <li>All Romans were either loyal to Caesar or hated him.</li> <li>Everyone is loyal to someone</li> <li>Vii. Everyone is loyal to someone</li> <li>Viii. People only try to assassinate rulers they are not loyal to.</li> <li>Wiii. Marcus tried to assassinate Caesar.</li> <li>a) Translate these sentences into formulas in predicate logic?</li> </ul>	10	CO2	L1

		b) Prove was Marcus loyal to Caesar?			
	b)	Illustrate in detail about forward and backward reasoning and solve the given example using forward and backward reasoning.	04+06	CO2	L2
		Given facts: A, B, C, E, G, H Rules:			
		F&B->Z C&D->F A->D			
	a)	OR OR			
4	a)	Consider the following set of well-formed formulas in predicate logic: Man(Marcus) Pompeian(Marcus) ∀x: Pompeian(x)→Roman(x) Ruler(caeser) ∀x:Roman(x)→[loyal(x,caeser)∨hate(x,caeser) ∀x:y loyalto(x,y) ∀x: ∀y:(man(x))∧rular(y)∧Tryassissinate(x,y))→Loyalto(x,y) Tryassissinate(Marcus, Caeser) Convert these into clause form and prove that hate("Marcus,Caeser") using Resolution proof	10	CO2	L1
	b)	<ul> <li>i) List the difference between Procedural and Declarative knowledge.</li> <li>ii) What is matching in rule based system? Briefly explain the different proposals for matching.</li> </ul>	04+06	C01	L2



### STAFF ALLOTMENT LIST -INTERNALS

#### 

Date: 07/02/2024

SEM: V

SI.No	Name of the Faculty	The second s	2/2024	09/02	2/2024	10/02	/2024		
Chi 10	ivanie of the Faculty	MS	AS	MS	AS	MS	AS	FACULTY SIGN	
01	Dr. Chandrashekara S N								
02	Dr. DeepikaLokesh								
03	Dr. VandanaDubey								
04	Narayanaswamy H		302					62-	
05	Vasudeva R								
06	Raja David			207			208	Raja	
07	Manjunath Singh		207			-	302	Magela	
08	Kavitha N				203			Carles	
09	Swathi J A					302	207	aw	
10	Ashok Babu A	207		3	204			Adula	
11	Praveen P		208				204	Praveen	
12	Vanitha L B				207	208		YS-	
13	Manjula S S			208		204		nt_	
14	Ayesha Sana				208	207		ST.	
15	Vyshnavi	203						a	
16	Stella	208		204	302			N	
17	Shwetha B	204	203	203			203	Shuttha B	
18	Lalitha M	302	204	302		902		bati Etc -	

Prepared by	Prof. Sarika CG and Prof. Kirthika K	Approved by	HOD
Date&sign	Goi Kinthih	Date&sign	level

Department of Computer Science and Engineering

Total: 30

Internals Seat Allotment FEB /2024

DATE:5-02-2024

Room No: 207

5TH	5TH	5TH	5TH	5TH	5TH
1CK21CS001	1CK21CS007	1CK21CS014	1CK21CS020	1CK21CS025	1CK21CS031
1CK21CS002	1CK21CS009	1CK21CS015	1CK21CS021	1CK21CS026	1CK21CS032
1CK21CS004	1CK21CS010	1CK21CS016	1CK21CS022	1CK21CS027	1CK21CS034
1CK21CS005	1CK21CS0012	1CK21CS018	1CK21CS023	1CK21CS028	1CK21CS035
1CK21CS006	1CK21CS013	1CK21CS019	1CK21CS024	1CK21CS030	1CK21CS036

Test Coordinator

HOD

## Department of Computer Science and Engineering

Total: 30

Internals Seat Allotment FEB/2024

DATE:5-02-2024

Room No: 208

5TH	5TH	5TH	5TH	5TH	5TH	
1CK21CS038	ICK21CS043	ICK21CS048	1CK21CS054	ICK21CS059	1CK21 CS065	
1CK21CS039	1CK21CS044	1CK21CS050	1CK21CS055	1CK21CS061	1CK21C8000	
1CK21CS040	1CK21CS045	1CK21CS051	1CK21CS056	1CK21CS062	1CK21CS06	
1CK21CS041	1CK21CS046	1CK21CS052	1CK21CS057	1CK21CS063	1CK21CS069	
1CK21CS042	1CK21CS047	1CK21C8053	1CK21CS058	1CK21CS064	1CK21CS070	

Test Coordinator

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HO

## Department of Computer Science and Engineering

Total: 30

Internals Seat Allotment FEB/2024

DATE:5-02-2024

Room No: 203

5TH	5TH	5TH	5TH	5TH	5TH	
ICK21CS071	1CK21C5076	ICK21C5082	1CK21CS087	1 CK21 CS094	10142105099	
1CK21CS072	ICK2ICS077	1CK21C5083	1CK21C5088	ICK21CS095	1CK21CS100	
1CK21CS073	1CK21CS078	1CK21CS084	1CX21CS090	1CK21CS096	1CK21CS101	
1CK21CS074	1CK21CS079	1CK21CS085	1CK21CS092	1CK21CS097	1CK21CS102	
1CK21CS075	1CK21CS080	1CK21CS086	1CK21CS093	1CK21CS098	1CK21CS103	

Test Coordinator

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Department of Computer Science and Engineering

Total:

1

29

Room No: 204

DATE:5-02-2024

Internals Seat Allotment FEB/2024

5TH	5TH	5TH	5TH	5TH	5TH	
1CK21CS104	1CK21CS109	1CK22CS401	1CK22CS406	1CK21AI002	1CK21AI008	
1CK21CS105	1CK21CS110	1CK22CS402	1CK22CS407	1CK21AI004	1CK21AI009	
1CK21CS106	CK21CS106 1CK21CS111		1CK22CS408	1CK21AI005	1CK21AI010	
1CK21CS107	1CK20CS022	1CK22CS404	1CK22CS409	1CK21AI006	1CK21AI011	
1CK21CS108	1CK22CS400	1CK22CS405	1CK21AI001	1CK21AI007		

Test Coordinator

Department of Computer Science and Engineering

Total: 30

Internals Seat Allotment FEB/2024

DATE:5-02-2024

Room No: 302

5TH	5TH	5TH	5TH	5TH	5TH
1CK21AI012	1CK21AI018	1CK21AI023	1CK21AI028	1CK21AI033	1CK21AI039
1CK21AI013	1CK21AI019	1CK21AI024	1CK21AI029	1CK21AI034	1CK21AI040
1CK21AI014	1CK21AI020	1CK21AI025	1CK21AI030	1CK21AI035	1CK21AI041
1CK21AI015	1CK21AI021	1CK21AI026	1CK21AI031	1CK21AI037	1CK21AI042
1CK21AI017	1CK21AI022	1CK21AI027	1CK21AI032	1CK21AI038	1CK21AI044

Test Coordinator

11 1110 HOD

Format 06#form#02a

	Artificial Intelligence & Machine Learning	Test No.	I	Name of the Faculty	SARIKA C G
Sub Code	18CS71	Date	02/11/2023		20/10/2010
Note: i)	Attach a copy of Internal Assessment		02/11/2025	Date	23/10/2023

Note: i) Attach a copy of Internal Assessment Question Paper along with the scheme

1

ii) Question wise split up marks allotted for different steps of answers is to be mentioned properly

Q. No.	Solution		Marks
1 a	Part A		<u> </u>
14	Artificial intelligence is the study of how make computers moment. It refers to the intelligence controlled by a compute Mundane Tasks • Perception	to do things which people do better at the er machine.	02
•	<ul> <li>Vision</li> <li>Speech</li> <li>Natural language</li> <li>Understanding</li> <li>Generation</li> <li>Translation</li> <li>Commonsense reasoning</li> <li>Robot control</li> </ul>		02
	Formal Tasks	Expert Tasks	03
-		ngineering - Design	
All Sector	- Checkers -Go	- Fault finding	03
	Mathematics     Geometry     ea	<ul> <li>Manufacturing planning</li> </ul>	
	- Logic	ientific analysis	
		edical diagnosis	
	<ul> <li>Proving properties of programs</li> <li>Fit</li> </ul>	nancial analysis	
	Some of the Task Domains of Artij	ficial Intelligence	
•	<ul> <li>Game Playing and theorem proving share the property considered to be displaying intelligence.</li> <li>Another important foray into AI is focused on Comm about physical objects and their relationships to each and other consequences.</li> </ul>	onsense Reasoning. It includes reasoning	
	To investigate this sort of reasoning No well Shaw ar (GPS) which they applied to several common sense t symbolic manipulations of logical expressions. But with a large amount of knowledge about a particular were selected.	asks as well as the problem of performing no attempt was made to create a program	

### Format 06#form#02a

Procedural Knowledge	Declarative Knowledge	
	Knowledge about "that something is true or false". For e.g: (i) Peter is older than Robert .	
first (ind their age.	(ii) Knowledge of the month when a crop	10
(ii) Procedure to harvest a crop	should be harvested	
In procedural representation they say how they will be examined.	in declarative, they does not say how they will be examined.	0
Focuses on tasks that must be performed to reach a particular objective or goal.	Refers to representation of objects and events.	0
High efficiency H	ligher level of abstraction	
Low in modifiability G	ood modifiability and good readability	01
Followed in C++ and cobol F	ollowed by sql	
Matching: matching between the current a) Indexing One way to select applicable rules comparing each one's preconditio match. But there are two problems	is toi do a simple search through all the rules, ns to the current state and extracting all the ones that	01
<ul> <li>In order to solve problem, Scanning through all of the inefficient.</li> <li>It is not always immediate</li> </ul>	it will be necessary to use a large number of rules. em at every step of the search would be hopelesly ly obvious whther a rule's preconditions are satisfied	02
<ul> <li>b) Matching with variables The problem of selecting applicab not stated as exact descriptions of</li> </ul>	le rules is made more difficult when preconditions are	01
A more complex matching process	ching is required when the preconditions of a rule specify ted explicitly in the description of the current state.	
<ul> <li>Preferences based on rule</li> <li>Specificity of the rule</li> <li>Physical order of th</li> <li>Preferences based on object</li> <li>Importance of object</li> </ul>	e rules ects	09
<ul> <li>Position of objects</li> <li>Preferences based on state</li> </ul>	es ng among them is to fire all of them temporarily and to	

Scrutinized by:

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Subject Name of F	A.C.S aculty DATE 2)11 23	ML Sar:Ka Invigilator Signature	Mam. MARKS MAX	Sub. Co Dept OBTAINED	C.S.E. SIGNATU	URE
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## Visvesvaraya Technological University

(State University of Government of Kamataka Established as per the VTU Act, 1994) "Juana Sangama" Belagavi-590018, Karnataka, India

Dr. T.N. Sreenivasa BE.,ME., PhD., FIE, CEng. Registrar (Evaluation) Phone : (0831) 2498131 Fax : (0831) 2498184

Ref.No/VTU/Exam/QPDS/2023-2024/ 166

Date: - 5 MAY 2023

## CIRCULAR

### Sub: Time Table for VII-Semester Examinations, June/July 2023.

The Time Table for eligible students of VII-Semester (All schemes), B.E./B.Tech. (June/July 2023) Examinations is published herewith and is also available on the VTU Website https://vtu.ac.in.

The Principals of all the constituent and affiliated Engineering Colleges are requested to go through the time table and bring the contents of the same to the notice of all the concerned.

### Sd/-Registrar (Evaluation)

To.

The Principals of all the affiliated Engineering Colleges and constituent Engineering College.

### C.W.C. :

- 1. The Hon'ble Vice Chancellor, through the Secretary to VC, VTU, Belagavi, for kind information.
- 2. The Registrar, VTU, Belagavi, for kind information.
- 3. The Regional Directors, R.O. Bengaluru /Belagavi /Kalaburagi /Mysuru, for kind information.
- 4. The Director, ITISMU, VTU Belagavi, for information and needful.

Registrar (Evaluation)

## Visvesvaraya Technological University, Belagavi

Time Table for Only Eligible Students of B.E./B.TECH. Examinations, June / July 2023

Date	2010 Scheme	2015 Scheme (CBCS)	2017 Scheme (CBCS)	2018 Scheme (CBCS)
	2.00pm to 5.00pm	2.00pm to 5.00pm	2.00pm to 5.00pm	2.00pm to 5.00pm
29-05-2023	10**71	15**71	17**71	18**71
31-05-2023	10**72	15**72	17**72	18**72
02-06-2023	10**73	15**73	17**73	18**73*
05-06-2023	10**74	15**74/74*	17**74/74*	18**74*
07-06-2023	10**75*	15**75*	17**75*	18**75*
09-06-2023	10**76*	15**76*	17**76*	
12-06-2023	10EVR77			

(VII - Semester of All Schemes)

Registrar (Evaluation)

	C. BYREGOV	<b>NDA IN</b>	STITI	<b>UTE O</b>	F TEC	HNOI	OGY,	Kolar	•				
	VTU JANURA	RY - 2024	4 Theor	y Exami	nation	Invigilat	tion All	otment					
	MORNING SESSION		4 Theory Examination Invigilation Allotment AFTERNOON SESSION										
	<b>REPORTING TIME: 9:00</b>	AM	REPORTING TIME: 1:30PM										
	EXAM TIMING: 9.30AM to 12.3	EXAM TIMING: 2.00PM to 05.00PM											
			JANURARY 2024										
SL. No	. Faculty Name	DEPT.	22	23 *	24	25	27	29	* 30	31			
			MON	TUE	WED	THU	SAT	MON	TUE	WED			
1	SATISH M S	ME	A					A	TOL	WED			
2	CHOWDAREDDAY C	ME			Α								
3	SRINATH K T	ME	A					M					
4	SANDEEP S N	ME			м		A	IVI					
5	SREEVIDYA N	ME		М			A						
7	VASUDEVA R	CSE								M			
8	MANJUNATH SINGH H	CSE					Α.			M			
11	KAVITHA N	CSE					A , M						
12	SWATHI J A	CSE					IVI			A			
13	ASHOK BABU AMBEDKAR A	CSE	A					М		A			
14	PRAVEEN P	CSE								M			
15	KIRTHIKA K	CSE	A				М						
16	MANJULA S S	CSE	A							A			
17	VANITA L B	CSE			M				A				
18	SARIKA C G	CSE	A		M								
19	AYESHA SANA	CSE			. IVI		A						

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20	VEDAVATHI	CV		A	1		1	M	
21	NARAYANASWAMY K A	CV	Α		1		A		
22	MONISH N V	CV		A				M	
23	SMITHA R	CV	Α		м				
24	ARUN KUMAR P	CV	Α		A				
26	SUKUMAR B S	ECE					м		
27	EASWARA M	ECE					м		А
28	SAMSON JOHN	ECE						A	
29	JAGADISH KUMAR G M	ECE	Α		A				
30	KAVYA.S	ECE	A		М				
31	NAGARAJ	MAT	A				A		м
32	CHANDAN	MAT	Α				A		A
33	PAVITHRA	MAT	М		Α			A	
34	CHAITHRA	MAT	М		А			A	
35	Dr ETHIRAJ	СНЕ			A		Α	A	
36	PRUTHIVI	CHE	М		Α			A	
37 '	RAMESH	PHY	М			М	•		
38	THANUJA	MAT	м		A			A	
39	ASHWINI	РНҮ	М					A	A

DCS 16/1/24

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CHIEF SUPRITENDENT Chief Superintendent C. Byre Gowda Institute of Technology KOLAR-563 101.

## C BYREGOWDA INSTITUTE OF TECHNOLOGI, KOLAR - 563 101. ROOM ALLOTMENT FOR FEB-2024 THEORY EXAMINATION

DATE :02-02-2024

SEMESTER : 1st SEM BE

TIME: 2.00 PM TO 5.00 PM

Room No.	Sem. / Branch/ Sub Code	REGIS			ERS A		ED	Total Allotted	Sem. / Branch / Sub Code	REGIS FC	TER N R THE	91.04 POP 49,2 10, PP			D	Total Allotted	No of Absents	Room Suptd. Signature
001	BMATS101	1CK23CS	001 006 011	002 007 012	003 008 013	004 009 014	005 010 015	15	BMATS101	1CK23AI	001 006 011	002 007 012	003 008 013	004 009 014	005 010 015	15	NIL	JUSN. 02/02/24
101	BMATS101	1CK23CS	016 021 026	017 022 027	018 023 028	019 024 029	020 025 030	15	BMATS101	1CK23AI	016 021 026	017 022 027	018 023 028	019 024 029	020 025 030	15	-N))	les 2/2/24
102	BMATS101	1CK23CS	031 036 041	032 037 042	033 038 043	034 039 044	035 040 045	15	BMATS101	1CK23AI	031 036 041	032 037 042	033 038 043	034 039 044	035 040 045	15	NEL	ta 1940 2/2/24
103	BMATS101	1CK23CS	046 051 056	047 052 057	048 053 058	049 054 059	050 055 060	15	BMATS101	1CK23AI 1CK22AI	046 051 056 025	047 052 035	048 053 040	049 054 044	050 055	15	NIL	Keithel 02/2/24.

Chief Superintendent C. Byre Gowda Institute of Technology KOLAR-563 101.

Room No.	Sem. / Branch/ Sub Code	REGISTER NUMBERS ALL TED FOR THE EXAMINATION							Sem. / Branch / Sub Code	REGISTER NUMBERS ALLOTED FOR THE EXAMINATION					Total Allotted	No of Absents	Room Suptd. Signature	
104	BMATS101	1CK23CS	061 066 071	067	063 068 073	069	065 070 075	15	BMATE101	1CK23EC	001 006 011	002 007 012	003 008 013	004 009 014	005 010 015	15	NIL	2/2/24
203	BMATS101	1CK23CS	076 081 087	077 082 088		079 084 090	080 086	14	BMATE101	1CK23EC	016 021 026	017 022 027	018 023 028	019 024 029	020 025 030	15	till	Alam Der
204	BMATS101	1CK23CS	091 096 101	092 097 102	093 098 103	094 099 104	095 100 105	15	BMATE101	1CK23EC	031 036 041 011	032 037 028	033 038 042	034 039	035 040	14	NIL	2/2/24
204A	BMATS101	1CK23CS	106 111 116		108 113 118		110 115 120	15	BMATC101	1CK23CV 1CK22CV	001 006 011 003	002 007	003 008	004 009	005 010	12	-114-	7412

Chief Superintendent C. Byre Gowda Institute of Technology KOLAR-563 101.

Room No.	Sem. / Branch/ Sub Code	REGISTER NUMBERS ALL TED FOR THE EXAMINATION							Sem. / Branch / Sub Code	REGISTER NO. BERS ALLOTED FOR THE EXAMINATION					D	Total Allotted	No of Absents	Room Suptd. Signature
205	BMATS101	1CK23CS	121 126 131	122 127 132	123 128 133	124 129 134	125 130 135	15	BMATS101	1CK23CE	001 006 011	002 007 012	003 008 013	004 009 014	005 010 015	15	in the second se	storbala
207	BMATS101	1CK23CS	136 141 146	137 142 147	138 143 148	139 144 149	140 145 150	15	BMATS101 BMATM101	1CK23CE 1CK23ME	016 021 001	017 022 002	018 023 003	019 024 004	020	9 5	NIL	- 2102/24
208	BMATS101	1CK23CS	151 156 161	152 157 162	153 158	154 159	155 160	12	BMATS101	1CK22CS	050 (096) 137	057 108 153	066 110	072 119	088 133	12	01	Peley.

Chief Superintendent C. Byre Gowda institute of Technology KOLAR-563 101.